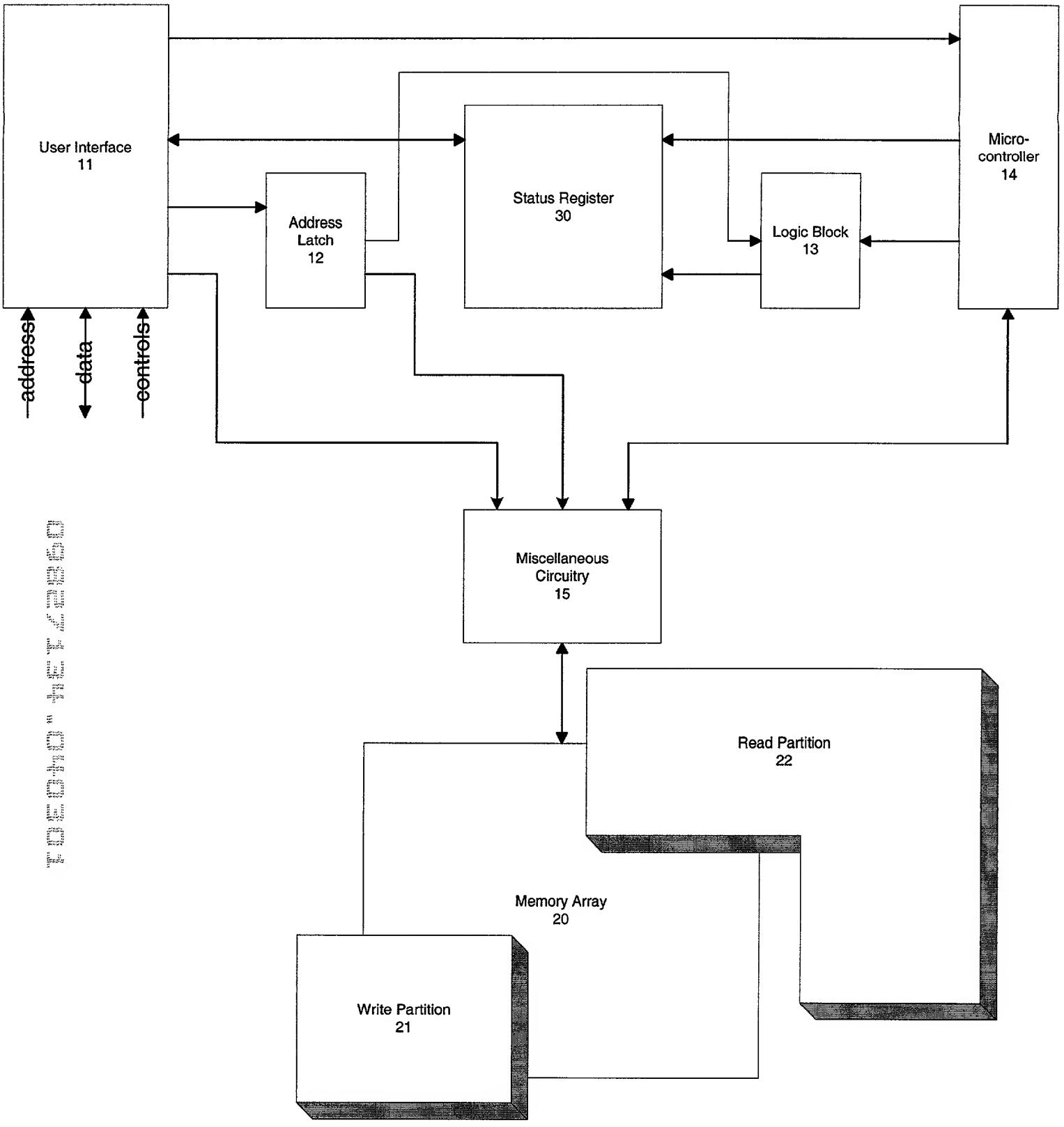
User Interface
11
Micro-
controller
14
Address
Latch
12
Status Register
30
Logic Block
13
address
data
controls
Miscellaneous
Circuitry
15
Read Partition
22
Memory Array
20
Write Partition
21

Figure 1

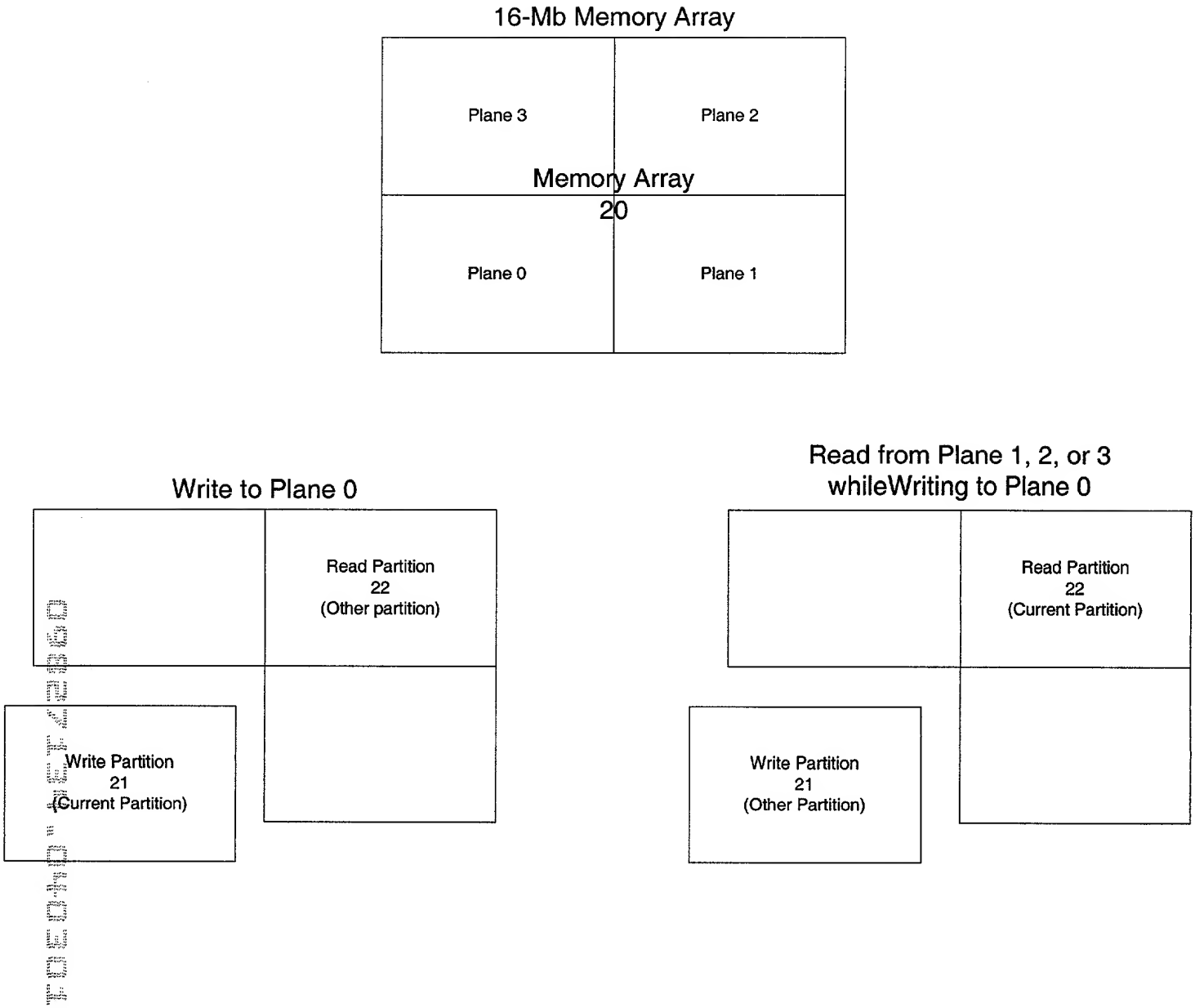
16-Mb Memory Array
Plane 3
Plane 2
Memory Array
20
Plane 0
Plane 1
Write to Plane 0
Read Partition
22
(Other partition)
Write Partition
21
(Current Partition)
Read from Plane 1, 2, or 3
whileWriting to Plane 0
Read Partition
22
(Current Partition)
Write Partition
21
(Other Partition)

Figure 2

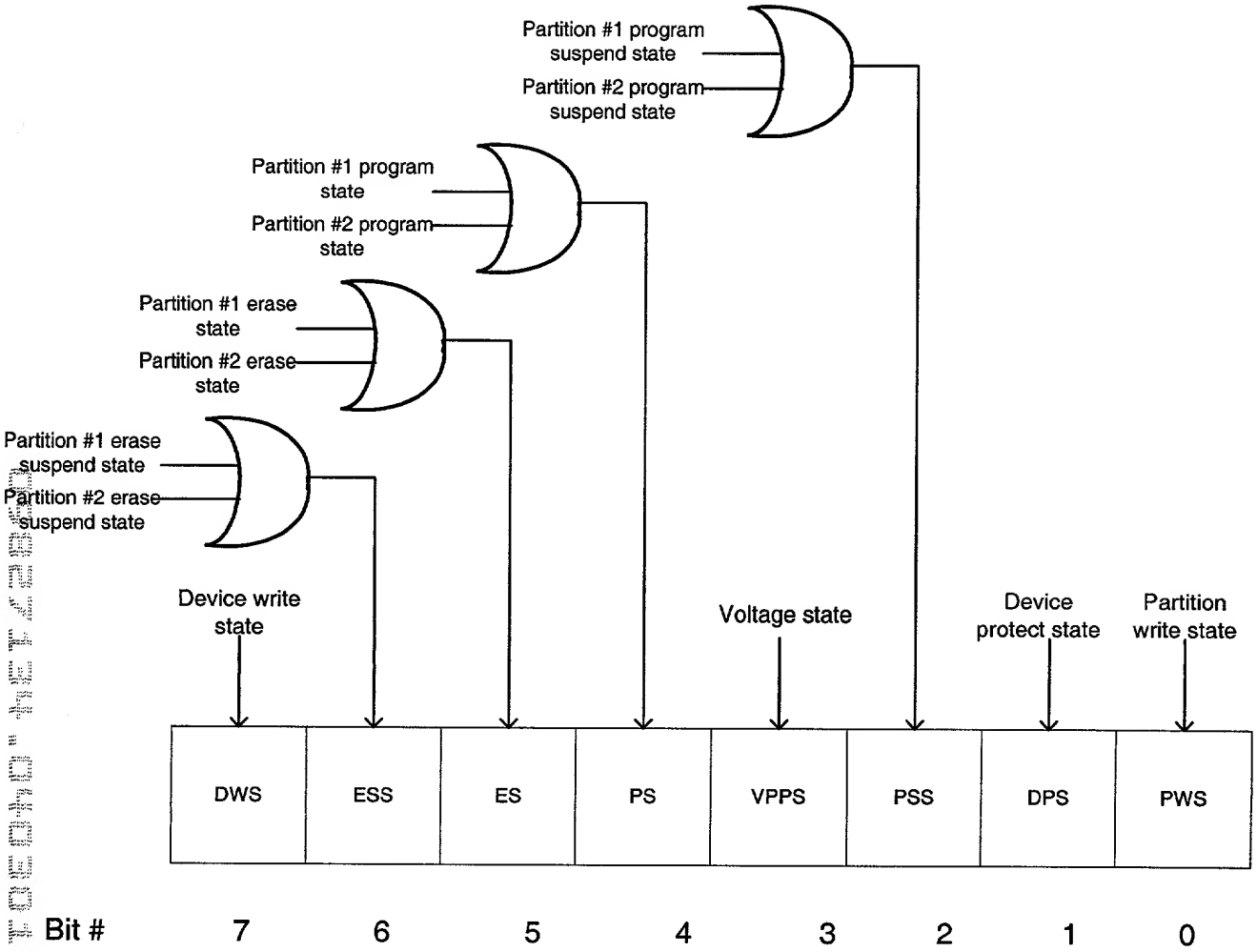
Partition #1 program suspend state
Partition #2 program suspend state
Partition #1 program state
Partition #2 program state
Partition #1 erase state
Partition #2 erase state
Partition #1 erase suspend state
Partition #2 erase suspend state
Device write state
Voltage state
Device protect state
Partition write state
DWS
ESS
ES
PS
VPPS
PSS
DPS
PWS
Bit #
7
6
5
4
3
2
1
0

Figure 3

DWS	PWS	Description
0	0	Addressed partition is performing Program/Erase operation. No other partition is active.
0	1	A partition other than the one currently addressed is performing a program/erase operation.
1	0	No program/erase operation is in progress in any partition. Erase and program suspend bits indicate whether other partitions are suspended.
1	1	Will not occur in standard program or erase mode.

Figure 4

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